

100-4917

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VALUE CLASSIFICATION

<p>O.I.P.E.</p> <p>AD SCANNED 109 G.A. Am</p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/912435	CONT/PRIOR F	CLASS 134 219	SUBCLASS 121.43	ART UNIT 1748 3742	EXAMINER Vayn Pascho
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Process for cleaning an integrated circuit package surface, for preparing the same for a subsequent ink marking process, and process for manufacturing an integrated circuit using such a cleaning process

PTO-2040
12/99

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ISSUING CLASSIFICATION										
ORIGINAL			CROSS REFERENCE(S)							
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
217		121.43	134	1.1						
INTERNATIONAL CLASSIFICATION										

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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